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Assistant Commissioner for Patents  
Washington, DC 20231  
ATTN: BOX PATENT APPLICATION

OFGS File No. : IR-1677 (2-1984)  
Inventor : Zhijun Qu and Kenneth Wagers  
Title : DUAL EPITAXIAL LAYER FOR HIGH VOLTAGE  
CONDUCTION POWER MOSFET DEVICES  
Assignee : International Rectifier Corporation

Enclosed herewith please find the following documents in the above-identified application for United States Letters Patent:

9 Pages of Specification including Abstract and Claims  
12 Numbered Claims Calculated as 12 Claims for Fee Purposes  
4 Sheets of Drawing Containing Figures 1 to 5.  
X Declaration and Power of Attorney  
Priority is Claimed under 35 U.S.C. §119:  
Convention Date \_\_\_\_\_ for \_\_\_\_\_ Appln. S.N. \_\_\_\_\_  
X Assignment  
Information Disclosure Statement, PTO-1449 and references  
X Return-Addressed Post Card

OFGS Check No. 25371, which includes the fee of \$800.00, calculated as follows:

Basic Filing Fee:	\$ 760.00
Additional Filing Fees:	
Total Number of Claims in Excess of 20, times \$18:	\$ -
Number of Independent Claims in Excess of 3, times \$78:	\$ -
One or More Multiple Dependent Claims: Total \$260:	\$ -
Total Filing Fees or	\$ 760.00
Total Filing Fee Reduced 50% for Small Entity:	
Assignment Recording Fee: \$40	\$ 40.00
TOTAL Filing Fee and Assignment Recording Fee:	<u>\$ 800.00</u>

In the event the actual fee is greater than the payment submitted or is inadvertently not enclosed, or if any additional fee during the prosecution of this case is not paid, the Patent and Trademark Office is authorized to charge the underpayment to Deposit Account No. 15-0700.

EXPRESS MAIL CERTIFICATE

Respectfully submitted,

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee (mail label #EL008624523US) in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, Attn: Box Patent Application, on June 9, 1999

Dorothy Jenkins

Name of Person Mailing Correspondence

Dorothy Jenkins  
Signature  
June 9, 1999  
Date of Signature

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# CERTIFICATION

I am the individual who sealed the envelope addressed to the Commissioner of Patents and Trademarks for the new application indicated below.

I certify that immediately before sealing the envelope, I checked its contents and made sure that each and every item listed on the transmittal letter was in the envelope. I then sealed the envelope.

I declare that all statements made herein of my own knowledge are true and were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of title 18 of the United State Code and that such willful statements may jeopardize the validity of the application or any patent issued thereon.

Dated: June 9, 1999

  
Fabrina Sheehan

OFGS File Number: IR-1677 (2-1984)

First Applicant: Zhijun Qu

Title: DUAL EPITAXIAL LAYER FOR HIGH  
VOLTAGE CONDUCTION POWER MOSFET  
DEVICES